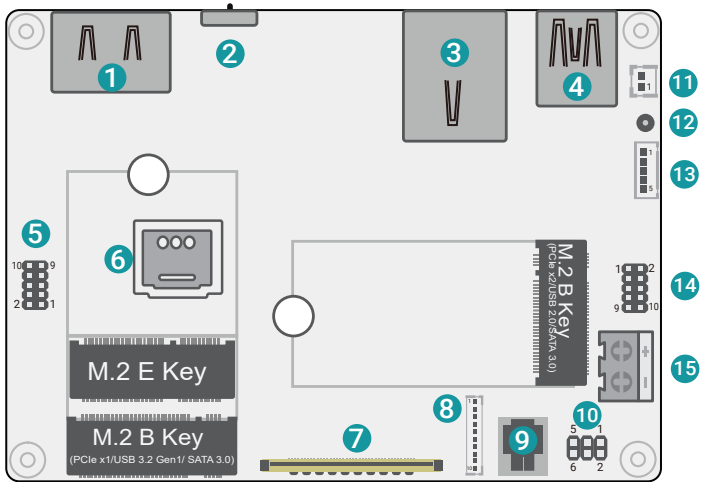
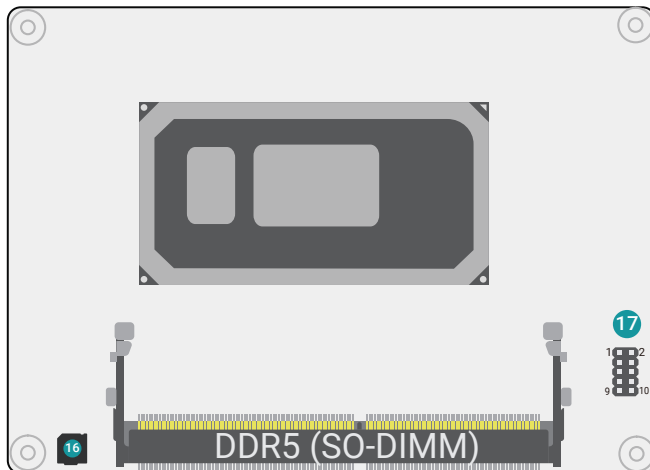


## Board Layout

### Top View



### Bottom View



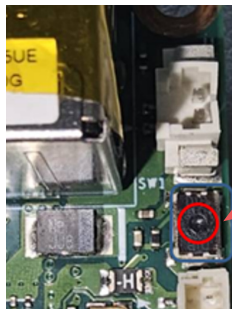
- |   |               |    |                            |
|---|---------------|----|----------------------------|
| 1 | DP++          | 9  | SPI                        |
| 2 | TDP Switch    | 10 | Front Panel                |
| 3 | 2.5G LAN      | 11 | RTC Battery                |
| 4 | USB3.2 Gen2   | 12 | Tact switch for Clear CMOS |
| 5 | COM1          | 13 | SMBus                      |
| 6 | SIM Card Slot | 14 | USB2.0                     |
| 7 | eDP           | 15 | 12V DC IN                  |
| 8 | DIO           | 16 | Buzzer                     |
|   |               | 17 | Front Audio                |



**Note:**

Tact Switch for Clear CMOS :

Turn off the power. Press and hold the switch for 1 or 2 seconds and then turn the power back on.



Use the tip of an unfolded paper clip (or similar object with a sharp tip) to gently press the switch circled in red.

# PIN Assignment

## 11 ▶ RTC Battery (J1)

Pin	Assignment	Pin	Assignment
1	RTC Signal	2	GND

## 15 ▶ 12V DC IN (CN1)

Pin	Assignment	Pin	Assignment
1	DC_IN	2	GND

## 13 ▶ SMBus (J5)

Pin	Assignment
1	3VSB
2	GND
3	I2C0_CLK
4	I2C0_SDA
5	I2C0_INT

## 8 ▶ DIO (CN6)

Pin	Assignment
1	D_IOA7_C
2	D_IOA6_C
3	D_IOA5_C
4	D_IOA4_C
5	D_IOA3_C
6	D_IOA2_C
7	D_IOA1_C
8	D_IOA0_C
9	5VSB
10	GND

## 10 ▶ Front Panel (J6)

Pin	Assignment	Pin	Assignment
1	SIO_PWSIN#	2	3V3SB
3	GND	4	SUS_LED#
5	SYSRST#	6	HD_LED#

## 17 ► Front Audio (AUJ1)

Pin	Assignment	Pin	Assignment
1	MIC2_L	2	AGND_AUDIO
3	MIC2_R	4	X
5	LINE2_R	6	MIC2-JD
7	AGND_AUDIO	8	X
9	LINE2_L	10	LINE2-JD

## 5 ► COM1 (TSJ1)

Pin	Assignment	Pin	Assignment
1	MDCD1#	2	MSIN1
3	MSOUT1	4	MDTR1#
5	GND	6	MDSR1#
7	MRTS1#	8	MCTS1#
9	MRI1#	10	X

## 14 ► USB2.0 (UBJ1)

Pin	Assignment	Pin	Assignment
1	5V_USB2_56	2	5V_USB2_56
3	USB2_4_C_N	4	USB2_3_C_N
5	USB2_4_C_P	6	USB2_3_C_P
7	GND	8	GND
9	X	10	X

### 3 ▶ LAN1 (ETCN2)

Pin	Assignment	Pin	Assignment
P1	X	P2	X
P3	225_LAN2_MDIP3	P4	225_LAN2_MDIN3
P5	225_LAN2_MDIP2	P6	225_LAN2_MDIN2
P7	225_LAN2_MDIP1	P8	225_LAN2_MDIN1
P9	225_LAN2_MDIP0	P10	225_LAN2_MDIN0
L1	225_LED2_LINK_ACT#	L2	3V3SB
L3	225_LED2_1000#	L4	225_LED2_2500#

### 4 ▶ USB3.0 (UBCN1)

Pin	Assignment	Pin	Assignment
1	5V_USBA_12	2	USB2_1_C_N
3	USB2_1_C_P	4	GND
5	USB3_1_C_RXN	6	USB3_1_C_RXP
7	GND	8	USB3_1_C_TXN
9	USB3_1_C_TXP	10	5V_USBA_12
11	USB2_2_C_N	12	USB2_2_C_P
13	GND	14	USB3_2_C_RXN
15	USB3_2_C_RXP	16	GND
17	USB3_2_C_TXN	18	USB3_2_C_TXP

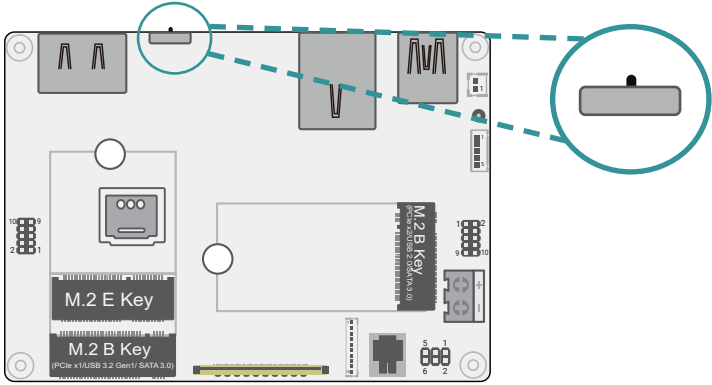
## 1 ▶ DP++ (CN11)

Pin	Assignment
1	DPA_LANE0_P_C
2	GND
3	DPA_LANE0_N_C
4	DPA_LANE1_P_C
5	GND
6	DPA_LANE1_N_C
7	DPA_LANE2_P_C
8	GND
9	DPA_LANE2_N_C
10	DPA_LANE3_P_C
11	GND
12	DPA_LANE3_N_C
13	DPA_AUX_SEL_COM
14	DP2_CEC
15	DPA_CLK_AUXP
16	GND
17	DPA_DATA_AUXN
18	DPA_HPD_R
19	GND
20	3V3

Pin	Assignment	Pin	Assignment
1	X	21	LVDS_PANEL_PWR
2	+12V	22	LVDS_PANEL_PWR
3	+12V	23	LVDS_PANEL_PWR
4	+12V	24	GND
5	+12V	25	eDPB_AUXN
6	X	26	eDPB_AUXP
7	X	27	GND
8	DIMMING_1	28	eDPB_LANE0_C_P
9	BLONOFF_1	29	eDPB_LANE0_C_N
10	eDP_GND	30	GND
11	eDP_GND	31	eDPB_LANE1_C_P
12	eDP_GND	32	eDPB_LANE1_C_N
13	eDP_GND	33	GND
14	eDP_HPD_CON_1	34	eDPB_LANE2_C_P
15	GND	35	eDPB_LANE2_C_N
16	GND	36	GND
17	GND	37	eDPB_LANE3_C_P
18	GND	38	eDPB_LANE3_C_N
19	X	39	GND
20	LVDS_PANEL_PWR	40	X

# Switch Setting

## 2 ▶ TDP Switch Setting (J3)



<b>Switch Setting</b>	TDP - Max Assured Power
<b>Note</b>	HW only, BIOS invalidate



<b>Switch Setting</b>	TDP - Base Power
<b>Note</b>	Both HW/BIOS is validate



<b>Switch Setting</b>	TDP - Min Assured Power
<b>Note</b>	HW only, BIOS invalidate



**Note:**

To enable the latest setting, please reset to validate.



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at [www.dfi.com/downloadcenter](http://www.dfi.com/downloadcenter), or via the QR codes to the right.

