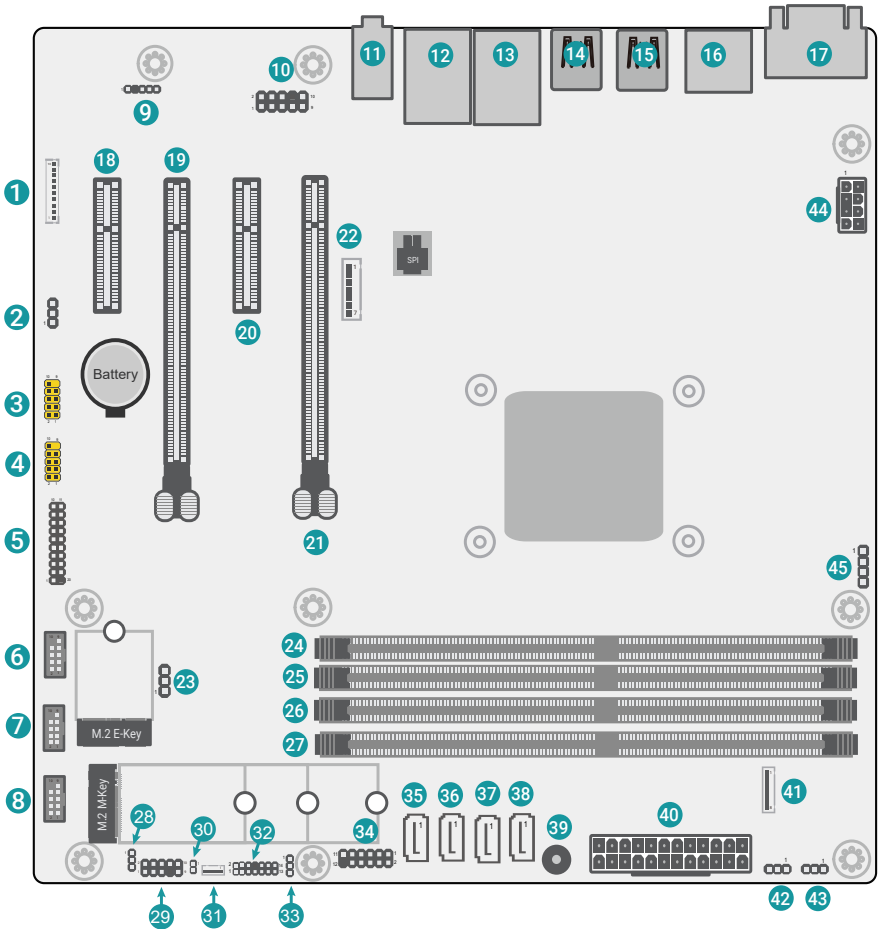


## Board Layout

### Top View



- 1 DIO

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- 2 System Fan3

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- 3 USB2\_12/13

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- 4 USB2\_10/11

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- 5 USB3\_6/7

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- 6 COM2

---

- 7 COM3

---

- 8 COM4

---

- 9 SPDIF

---

- 10 Front Audio

---

- 11 ▲MIC-In  
▼Line-Out  
▼Line-In

---

- 12 ▲LAN2  
▼USB2\_4/5  
USB3\_4/5

---

- 13 ▲LAN1  
▼USB2\_2/3  
USB3\_2/3

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- 14 USB3.2 Gen2

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- 15 USB2.0

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- 16 ▲DP++  
▼HDMI

---

- 17 ▲COM1  
▼VGA

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- 18 PCIE4

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- 19 PCIE3

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- 20 PCIE2

---

- 21 PCIE1

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- 22 OOB SPIROM Update

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- 23 M.2 E Power Select

---

- 24 DIMM1

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- 25 DIMM2

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- 26 DIMM3

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- 27 DIMM4

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- 28 M.2 M Power Select

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- 29 PS2

---

- 30 Case Open

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- 31 OOB I2C

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- 32 ESPI Header

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- 33 AT/ATX Mode Select

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- 34 Front Panel

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- 35 SATA3

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- 36 SATA2

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- 37 SATA1
- 38 SATA0
- 39 Buzzer
- 40 ATX Power
- 41 SMBus+I2C Header
- 42 System Fan1
- 43 System Fan2
- 44 +12V Power
- 45 CPU Fan

## Jumper Settings

33 AT/ATX Mode Select	JP2
AT mode (default)	1-2 On
ATX mode	2-3 On

28 M.2 M Power Select	JP3
3.3V_RUN (default)	1-2 On
3.3V_ALW	2-3 On

23 M.2 E Power Select	JP4
3.3V_RUN (default)	1-2 On
3.3V_ALW	2-3 On

# PIN Assignment

## 42 ▶ System Fan1 (J11)

Pin	Assignment
1	GND
2	PWM
3	TACH

## 43 ▶ System Fan2 (J12)

Pin	Assignment
1	GND
2	PWM
3	TACH

## 2 ▶ System Fan3 (J13)

Pin	Assignment
1	GND
2	PWM
3	TACH

## 30 ▶ Case Open (SOJ1)

Pin	Assignment
1	Case Open#
2	GND

## 45 ▶ CPU Fan (J9)

Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

## 9 ▶ S/PDIF (AUJ1)

Pin	Assignment
1	A5V
2	---
3	SPOUT
4	GND
5	SPIN

**38 ▶ SATA0 (J3)**

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

**37 ▶ SATA1 (J4)**

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

**36 ▶ SATA2 (J5)**

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

**35 ▶ SATA3 (J6)**

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

**22 ▶ OOB SPIROM Update (J1)**

Pin	Assignment
1	GPI_SPI_SW
2	SPI_SO-R
3	SPI_SI-R
4	SPI_CLK-R
5	SPI_CS0-
6	GND
7	1.8V_ALW

**41 ▶ SMBus Header (J37)**

Pin	Assignment	Pin	Assignment
1	3.3V_ALW	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_INT	6	I2C_CLK
7	I2C_SDA	8	I2C_INT

### 31 ► OOB I2C Header (J7)

Pin	Assignment
1	NC
2	GND
3	SIO_SCL
4	SIO_SDA
5	NC

### 10 ► Front Audio (AUJ2)

Pin	Assignment	Pin	Assignment
1	MIC2_L	2	GND
3	MIC2_R	4	NC
5	LINE2_R	6	MIC2_JD
7	GND	8	---
9	LINE2_L	10	LINE2_JD

### 17 ► COM1 (DPCN3)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### 6 ► COM2 (TSJ1)

Pin	Assignment	Pin	Assignment
1	MDCD-/RS422_RX+ / RS485_D+	2	MSIN-/RS422_RX- / RS485_D-
3	MSO-/RS422_TX+	4	MDTR-/RS422_TX-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### 7 ► COM3 (TSJ2)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### 8 ► COM4 (TSJ3)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-	10	----

### 1 ► DIO (J15)

Pin	Assignment
1	DI_3
2	DI_2
3	DI_1
4	DI_0
5	DO_3
6	DO_2
7	DO_1
8	DO_0
9	+5V_ALW
10	GND

### 5 ► USB3\_6/7 , USB2\_6/7 (UBJ1)

Pin	Assignment	Pin	Assignment
1	SBV3	11	USBP_C_9P
2	USB3_RX5_DN	12	USBP_C_9N
3	USB3_RX5_DP	13	GND
4	GND	14	USB3_TX4_DP
5	USB3_TX5_DN	15	USB3_TX4_DN
6	USB3_TX5_DP	16	GND
7	GND	17	USB3_RX4_DP
8	USBP_C_8N	18	USB3_RX4_DN
9	USBP_C_8P	19	SBV3
10	NC	20	---

### 3 ► USB2\_12/13 (UBJ2)

Pin	Assignment	Pin	Assignment
1	SBV6	2	SBV6
3	USBP_C_3N	4	USBP_C_6N
5	USBP_C_3P	6	USBP_C_6P
7	GND	8	GND
9	----	10	NC

### 4 ► USB2\_10/11 (UBJ3)

Pin	Assignment	Pin	Assignment
1	SBV6	2	SBV6
3	USBP_C_7N	4	USBP_C_10N
5	USBP_C_7P	6	USBP_C_10P
7	GND	8	GND
9	----	10	NC

### 29 ► PS2 Header (SOJ3)

Pin	Assignment	Pin	Assignment
1	PS2_KCLK	2	PS2_MCLK
3	PS2_KDAT	4	PS2_MDAT
5	GND	6	GND
7	----	8	GND
9	BVCC	10	BVCC

## 34 ► Front Panel (J8)

Pin	Assignment	Pin	Assignment
1	NC	2	3V3SB
3	3V3_RUN	4	3V3SB
5	HDD_LED	6	SUS_LED
7	GND	8	GND
9	SYS_RST-	10	PWR_BTN-
11	NC	12	---

## 32 ► eSPI Header (J14)

Pin	Assignment	Pin	Assignment
1	3V3SB	2	ESPI_CLK
3	ESPI_RESET#	4	GND
5	ESPI_ALT#	6	UART_TX
7	ESPI_D0	8	---
9	ESPI_D1	10	ESPI_CS
11	ESPI_D2	12	UART_RX
13	ESPI_D3	14	3V3SB



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at [www.dfi.com/downloadcenter](http://www.dfi.com/downloadcenter), or via the QR codes to the right.

