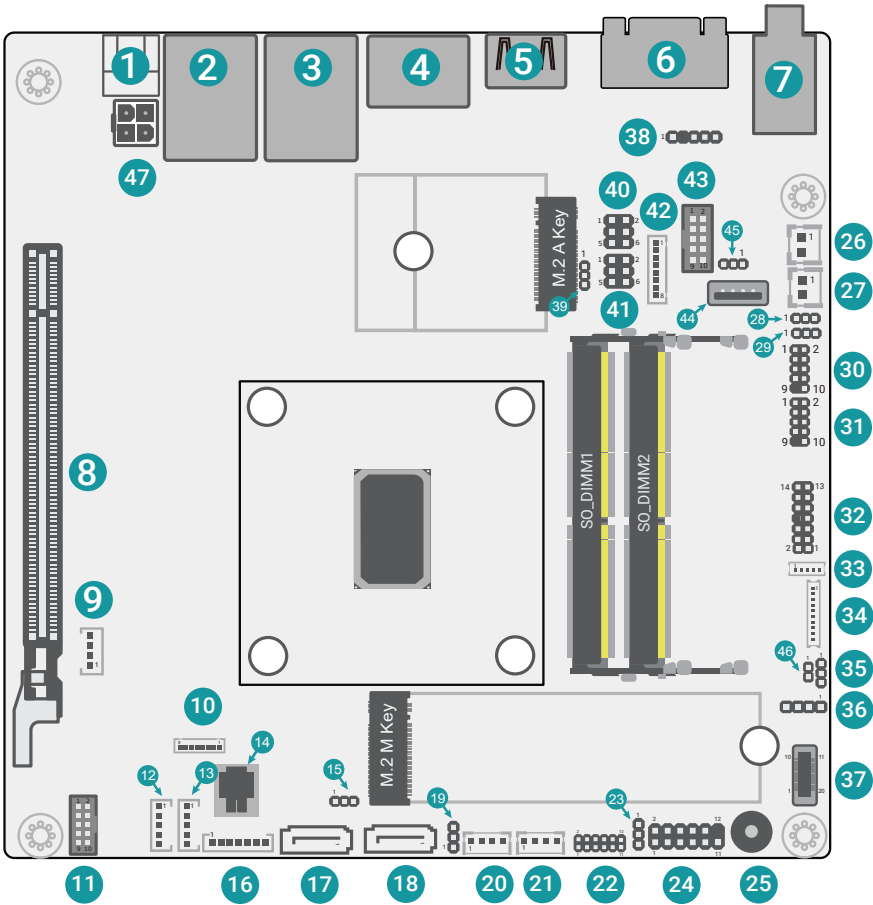


Board Layout

Top View



- 1 DC Jack for DC-IN
 - ▲LAN1
 - ▼USB2 2/3
 - ▼USB3 2/3
- 2 ▲LAN2
- 3 ▼USB2 4/5
- ▼USB3 4/5
- 4 ▲DP2
- ▼DP3
- 5 DP0/DP1
- 6 ▲COM1
- ▼COM2
- 7 ▲Mic In
- ▼Line Out
- ▼Line In
- 8 PCIE
- 9 CPU Fan
- 10 SMBus
- 11 COM3/4/5/6
- 12 SATA Power
- 13 SATA Power
- 14 SPI
- 15 M.2-M SATA or PCIE Model
- 16 OOB SPIROM Update
- 17 SATA0
- 18 SATA1
- 19 M.2-M Power
- 20 C.C.Talk
- 21 C.C.Talk
- 22 Simple-232 / TTL-5V
- 23 AT/ATX Mode Select
- 24 Front Panel
- 25 Buzzer
- 26 Speaker
- 27 Speaker
- 28 Panel Inverter Power
- 29 M.2-A Power
- 30 USB2
- 31 USB2
- 32 eSPI Header
- 33 OOB I2C
- 34 DIO
- 35 COM6
- 36 System Fan
- 37 For GM903
- 38 SPDIF
- 39 Panel Power ON/OFF
- 40 BKL,VDDEN

41 Panel Power Selection

42 Panel Power

43 For GM903

44 USB2.0 Type A (Vertical)

45 Panel Backlight Selection

46 CASEOPEN

47 2x2p Power Connector

Jumper Settings

29 M.2-A Power	JP2
3.3V_RUN (default)	1-2 On
3.3V_ALW	2-3 On

19 M.2-M Power	JP3
3.3V_RUN (default)	1-2 On
3.3V_ALW	2-3 On

23 AT/ATX Mode Select	JP4
AT mode (default)	1-2 On
ATX mode	2-3 On

35 COM6	JP5
RS232	1-2 On
TTL-3.3V	2-3 On

22 Simple-232 / TTL-5V	JP51
RS232(COM3)-default	1-3, 2-4 On
TTL-5V(COM3)	3-5, 4-6 On
RS232(COM4)-default	7-9 On
RS232(COM5)-default	8-10 On
C.C.Talk(COM4)	9-11 On
C.C.Talk(COM5)	10-12 On

15 M.2-M SATA or PCIE Model	JP1
SATA Connector/M.2-M PCIE	1-2 On
X /M.2- M SATA	2-3 On

45 Panel Backlight Selection	DPJP1
+3.3V_RUN (default)	1-2 On
+5V_RUN	2-3 On

28 Panel Inverter Power Selection	DPJP2
12V	1-2 On
5V (default)	2-3 On

39 Panel Power ON/OFF	DPJP3
Support Panel Power	1-2 On
NC	2-3 On

41 Panel Power Selection	DPJP4
12V	1-2 On
5V	3-4 On
3.3V (default)	5-6 On

40 BKL,VDDEN	DPJP5
LVDS	1-3,2-4
eDP	3-5,4-6

PIN Assignment

46 ▶ CASEOPEN (SOJ1)

Pin	Assignment
1	CASEOPEN#
2	GND

26 ▶ Speaker (J4)

Pin	Assignment
1	SPKOUT_R-
2	SPKOUT_R+

27 ▶ Speaker (J5)

Pin	Assignment
1	SPKOUT_L-
2	SPKOUT_L+

21 ▶ C.C.Talk (CN8)

Pin	Assignment
1	+12V_RUN
2	+12V_RUN
3	GND
4	CCTALK_CH1

20 ▶ C.C.Talk (CN9)

Pin	Assignment
1	+12V_RUN
2	+12V_RUN
3	GND
4	CCTALK_CH2

20 ▶ C.C.Talk (CN9)

Pin	Assignment
1	+12V_RUN
2	+12V_RUN
3	GND
4	CCTALK_CH2

36 ▶ System Fan (J16)

Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

9 ▶ CPU Fan (CN10) 38 ▶ SPDIF (AUJ1)

Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

Pin	Assignment
1	A5V
2	X
3	SPOUT
4	GND
5	SPIN

33 ► OOB I2C (J13)

Pin	Assignment
1	NC
2	GND
3	SIO_SCL
4	SIO_SDA
5	NC

16 ► OOB SPIROM Update (J3)

Pin	Assignment
1	GPL_SPI_SW
2	SPL_SO-R
3	SPL_SI-R
4	SPL_CLK-R
5	SPL_CS0-
6	GND
7	1.8V_ALW

12 ► SATA POWER (J6)

Pin	Assignment
1	5V
2	5V
3	12V
4	GND
5	GND

13 ► SATA POWER (J9)

Pin	Assignment
1	5V
2	5V
3	12V
4	GND
5	GND

20 ► SATA0 (J7)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

21 ► SATA1 (J8)

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

39 ▶ Panel Power (DPJ1)

Pin	Assignment
1	GND
2	GND
3	DIMMING
4	VCC_PANEL
5	+3.3V_RUN
6	BLONOFF
7	INVTER_PWR
8	INVTER_PWR

34 ▶ DIO (J18)

Pin	Assignment
1	DI3
2	DI2
3	DI1
4	DI0
5	DO3
6	DO2
7	DO1
8	DO0
9	+5V_ALW
10	GND

43 ▶ For GM903 (J20)

Pin	Assignment
1	GND
2	GND
3	USB2_D-
4	LINEOUT_R
5	USB2_D+
6	LINEOUT_L
7	GND
8	GND
9	RX
10	TX

30 ▶ USB2 (UBJ1)

Pin	Assignment
1	HUB_USB2
2	HUB_USB2
3	USBP_C_2N
4	USBP_C_3N
5	USBP_C_2P
6	USBP_C_3P
7	GND
8	GND
9	X
10	NC

31 ▶ USB2 (UBJ2)

Pin	Assignment
1	HUB_USB2
2	HUB_USB2
3	HUB_P1_N
4	HUB_P2_N
5	HUB_P1_P
6	HUB_P2_P
7	GND
8	GND
9	X
10	NC

11 ► COM3/4/5/6 (TSJ1)

Pin	Assignment	Pin	Assignment
1	CN_TX3	2	RS232_TX4
3	CN_RX3	4	RS232_RX4
5	GND	6	GND
7	RS232_TX5	8	RS232_TX6
9	RS232_RX5	10	RS232_RX6

10 ► SMBus Header (J2)

Pin	Assignment	Pin	Assignment
1	3.3V_ALW	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_INT	6	I2C_SCL
7	I2C_SDA	8	I2C_INT

24 ► Front Panel (J14)

Pin	Assignment	Pin	Assignment
1	NC	2	3V3SB
3	3V3_RUN	4	3V3SB
5	HDD_LED	6	SUS_LED
7	GND	8	GND
9	SYS_RST-	10	PWR_BTN-
11	NC	12	---

32 ► Front Panel (J14)

Pin	Assignment	Pin	Assignment
1	+3.3V_ALW	2	ESPI_CLK
3	ESPI_RESET#	4	GND
5	ESPI_ALT#	6	UART0_TX
7	ESPI_D0	8	---
9	ESPI_D1	10	ESPI_CS
11	ESPI_D2	12	UART0_RX
13	ESPI_D3	14	+3.3V_ALW

37 ► For GM903 (J19)

Pin	Assignment	Pin	Assignment
1	GND	11	GND
2	CLK_BUF_GM_P	12	+3.3V_RUN
3	CLK_BUF_GM_N	13	+3.3V_RUN
4	GND	14	GND
5	PCIE_PTX_P14	15	SMB_CLK_RESUME
6	PCIE_PTX_N14	16	SMB_DATA_RESUME
7	GND	17	GND
8	PCIE_RXP14	18	+3.3V_ALW
9	PCIE_RXN14	19	PCIE_RST-
10	+12V_RUN	20	+12V_RUN



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at www.dfi.com/downloadcenter, or via the QR codes to the right.

